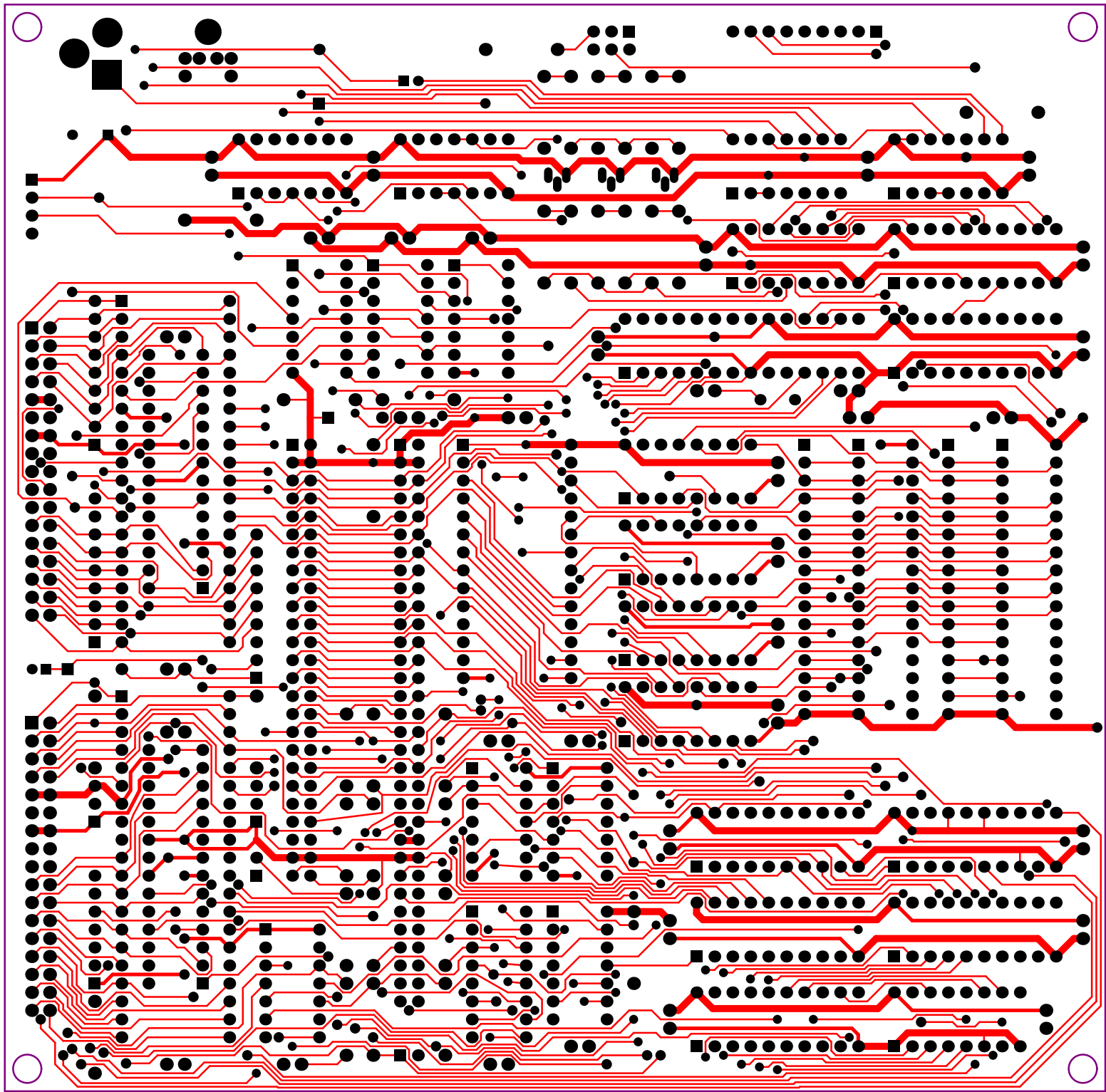
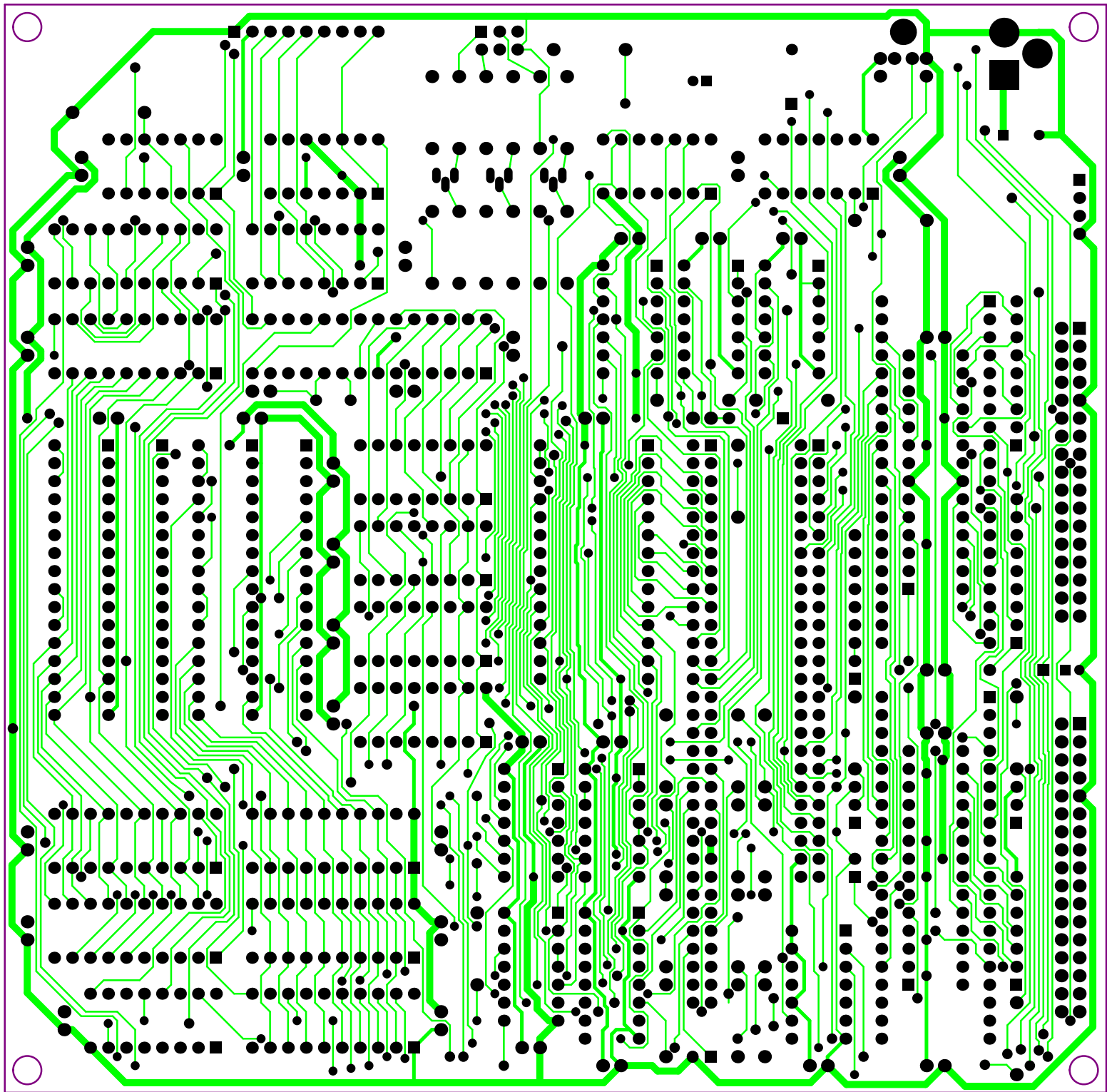
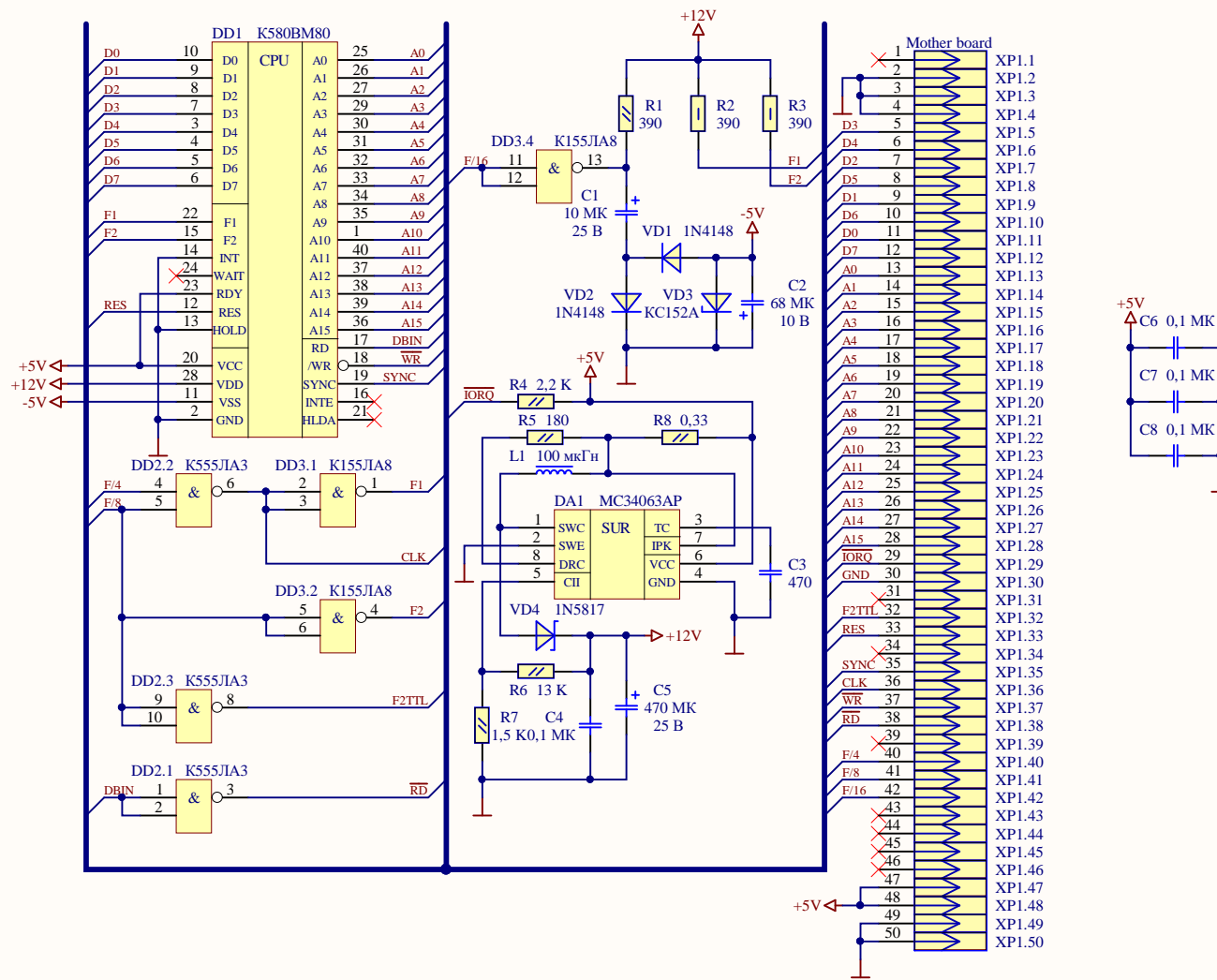


Title Сябр. Основная плата. Дополнения.		
Size: A4	Number: 2	Revision: V2.5
Date: 16.07.2020	Time: 22:44:31	Sheet 2 of 2
File:		









Title **Сяпр. 8080 CPU. CPUID=00**

Size: **A4**

Number:

Revision:

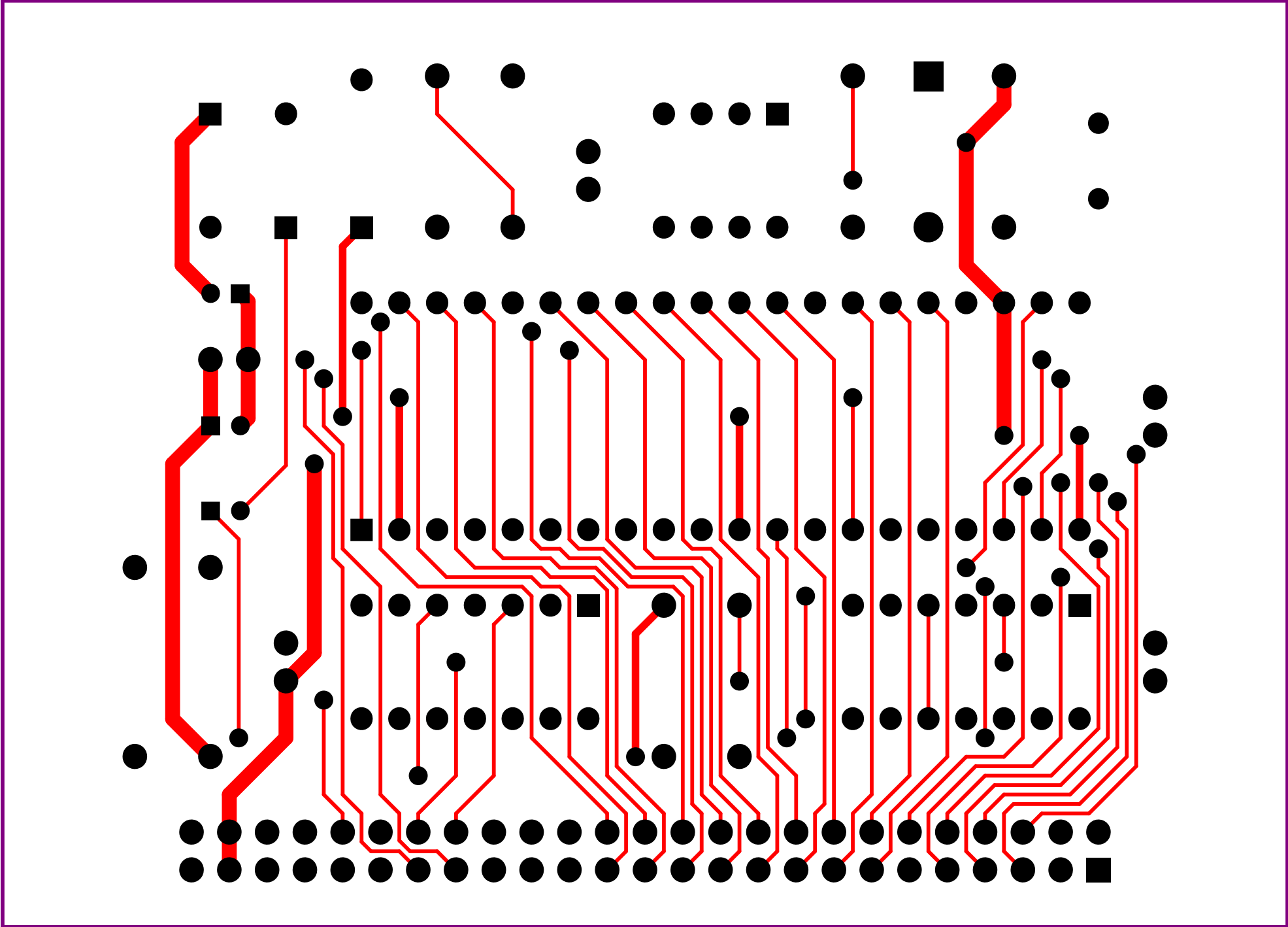
Date: 13.07.2020

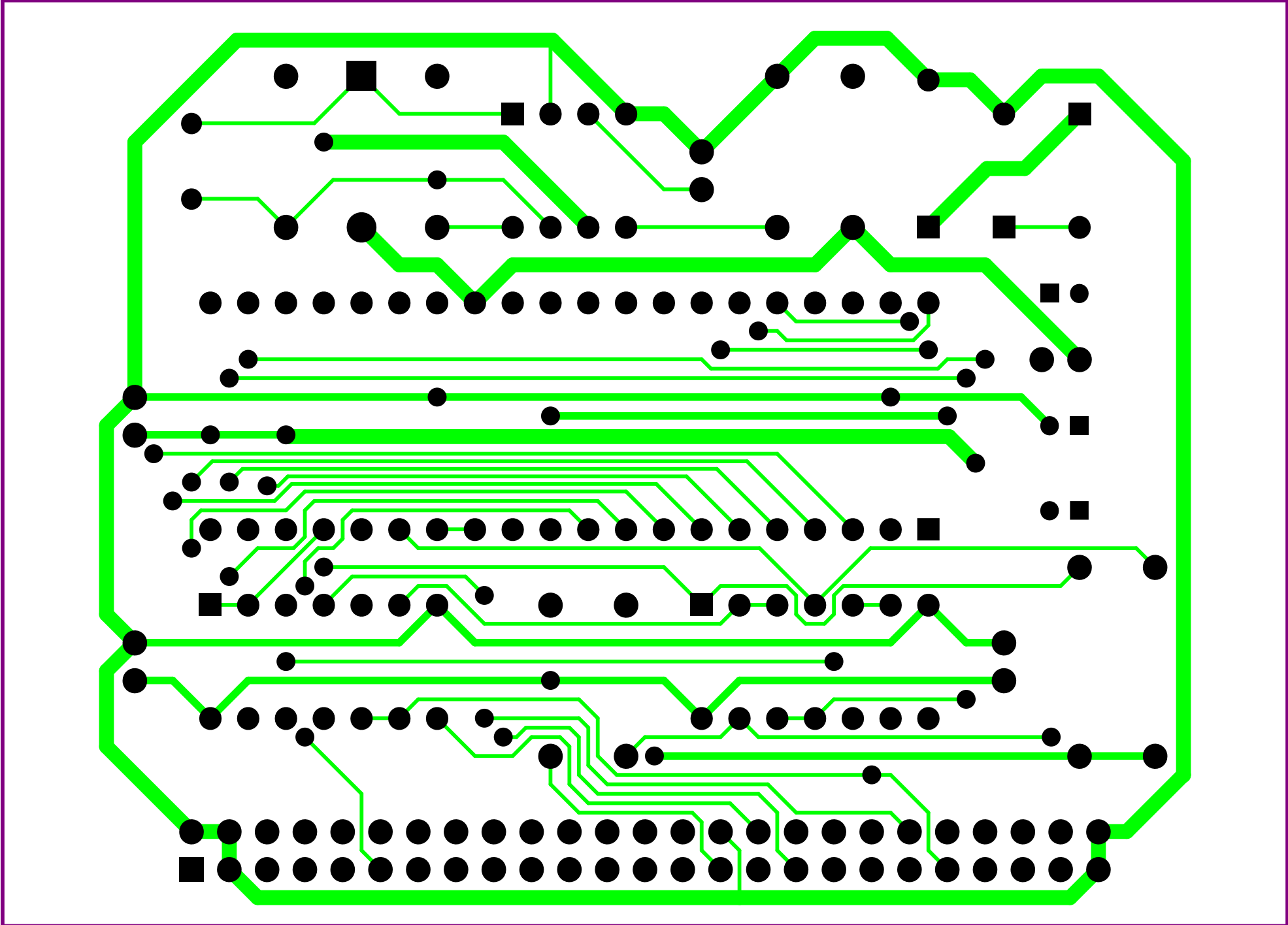
Time: 8:47:27

Sheet of

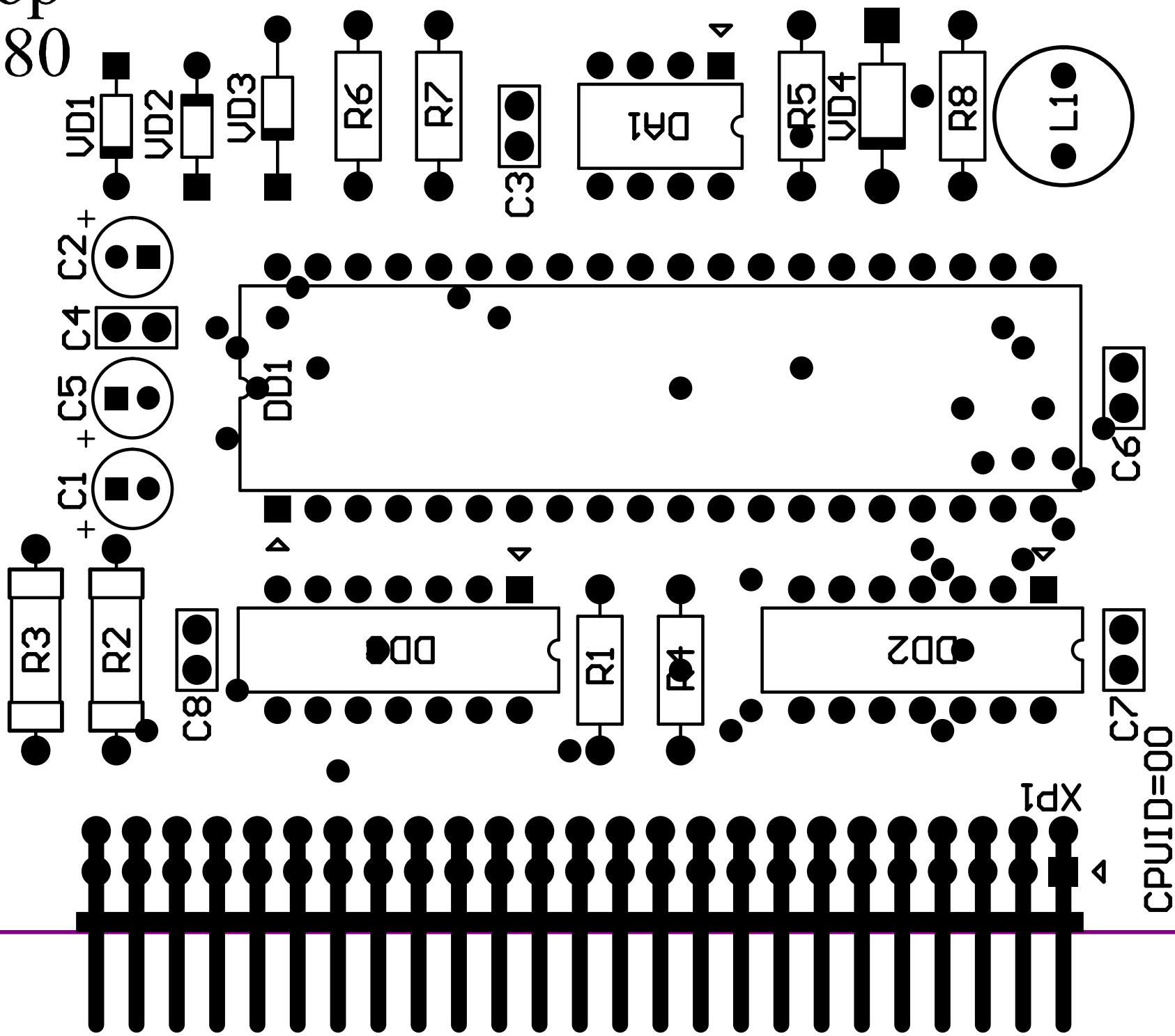
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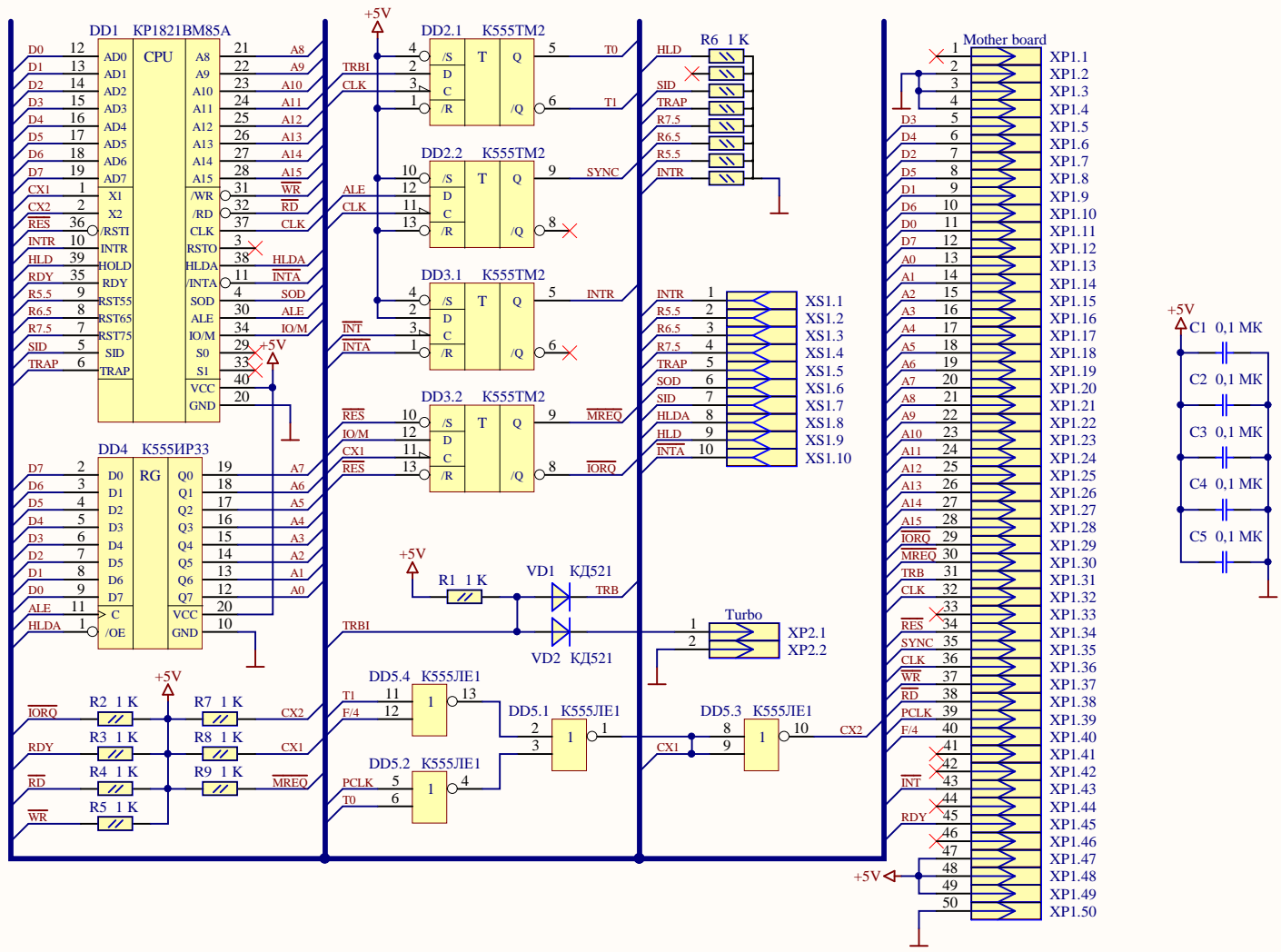




Сябр I8080



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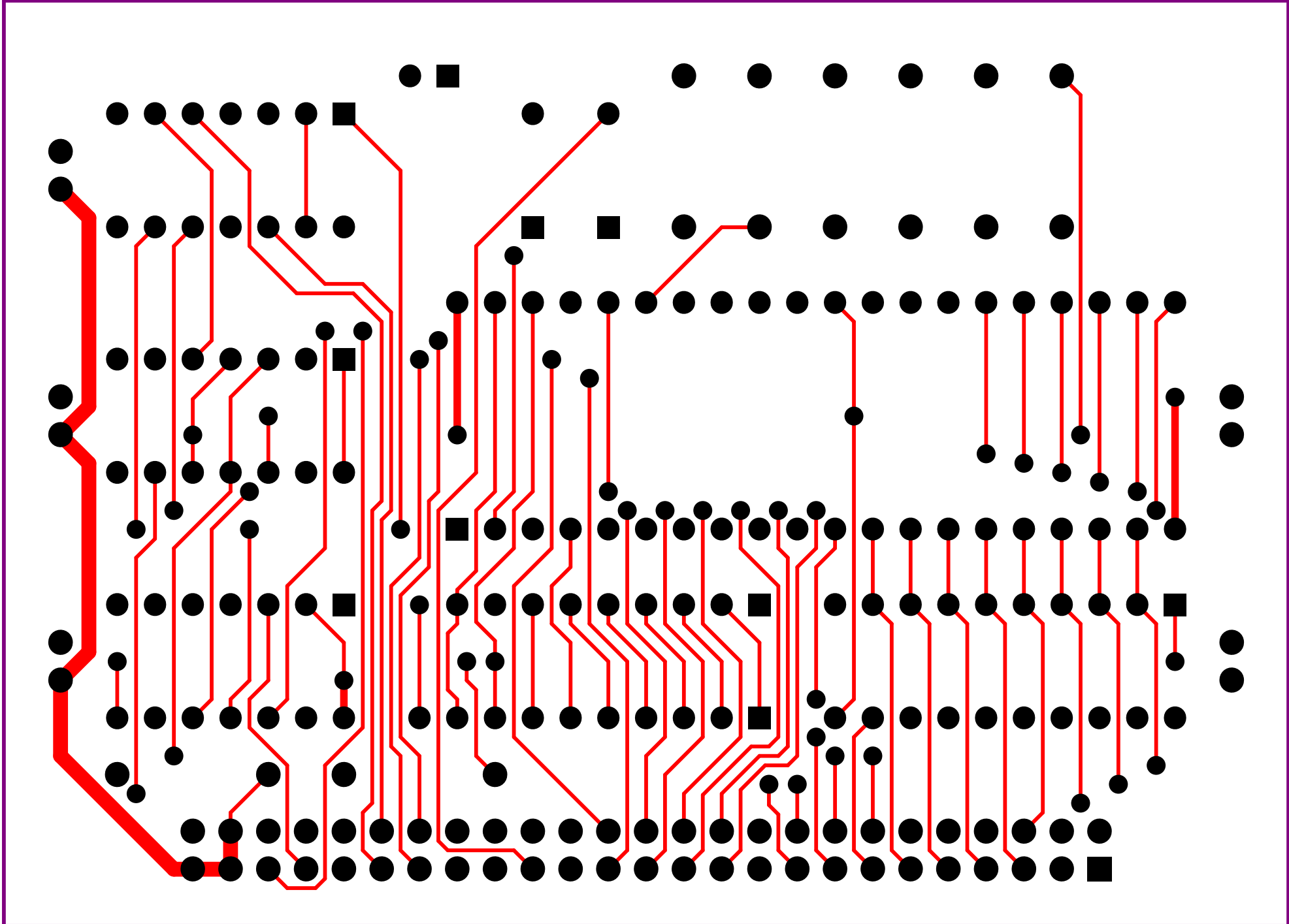
Title **Сяор. 8085 CPU. CPUID=10**

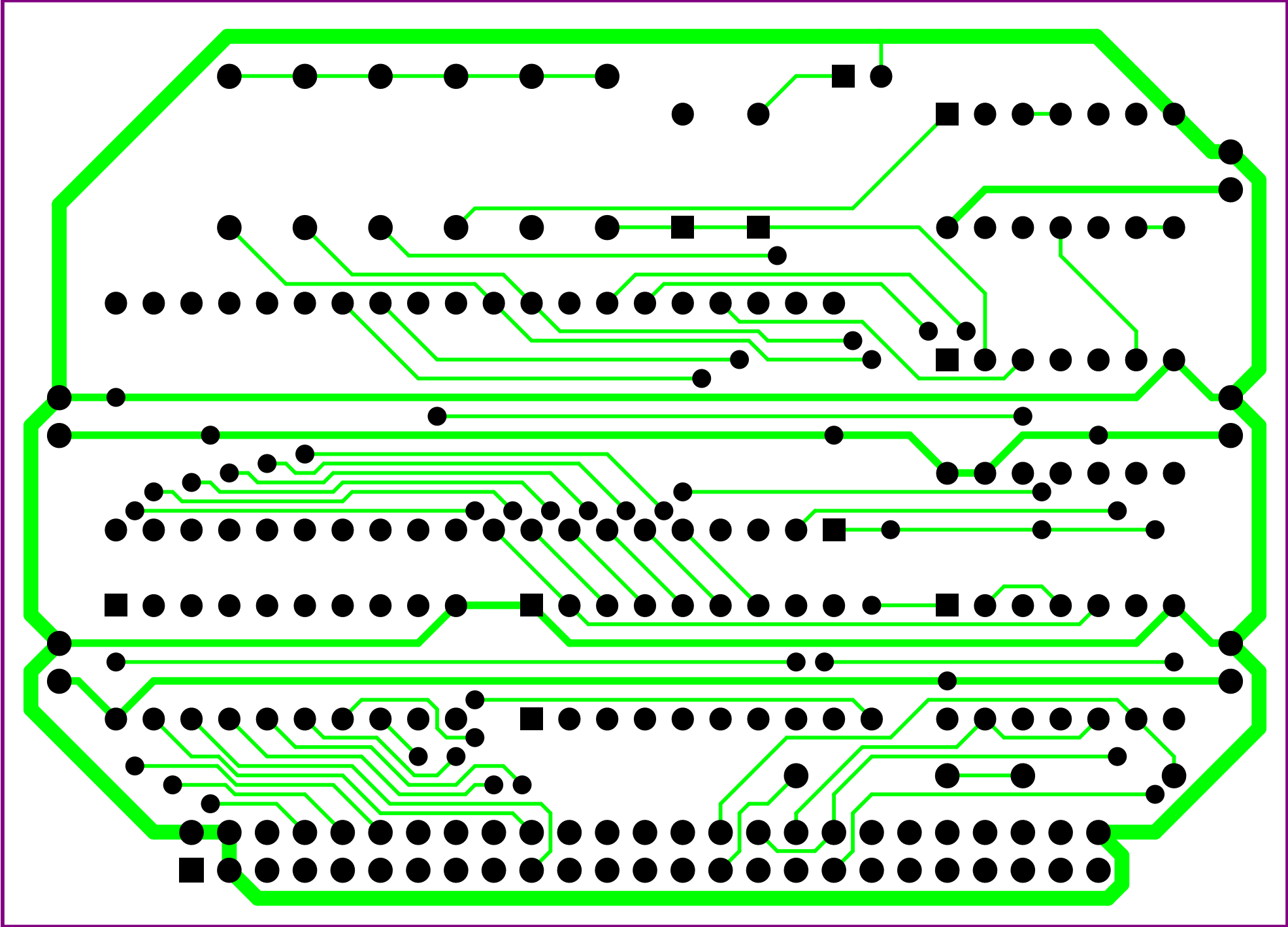
Size: **A4** Number: **3** Revision: *****

Date: **13.07.2020** Time: **9:13:42** Sheet **3** of **4**

File:



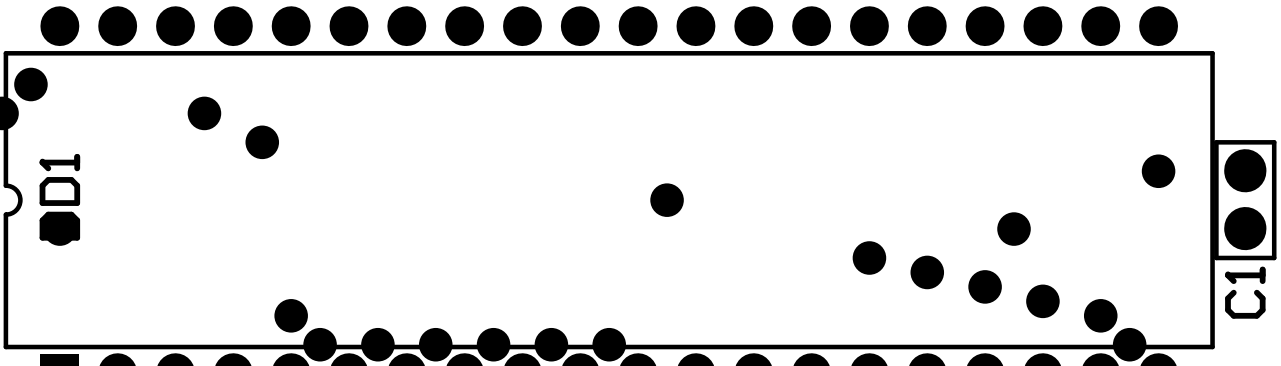
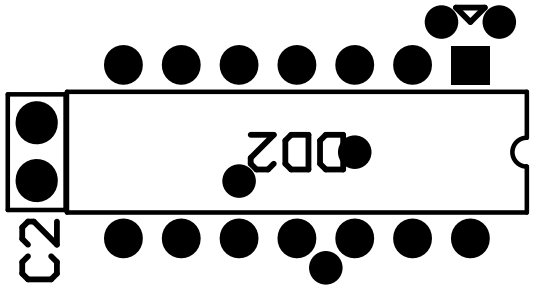
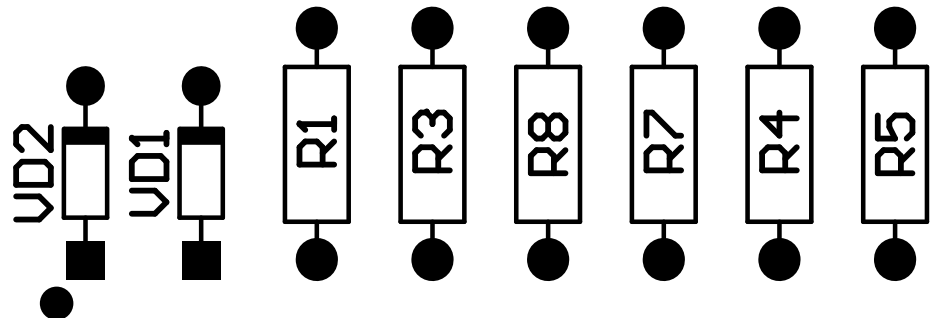
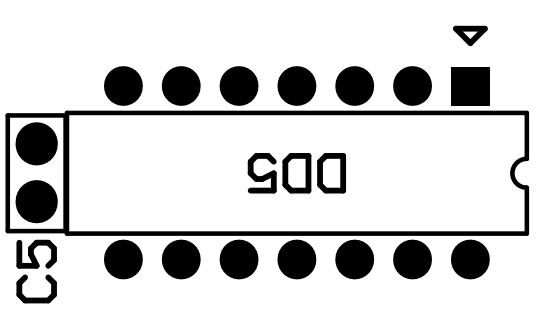




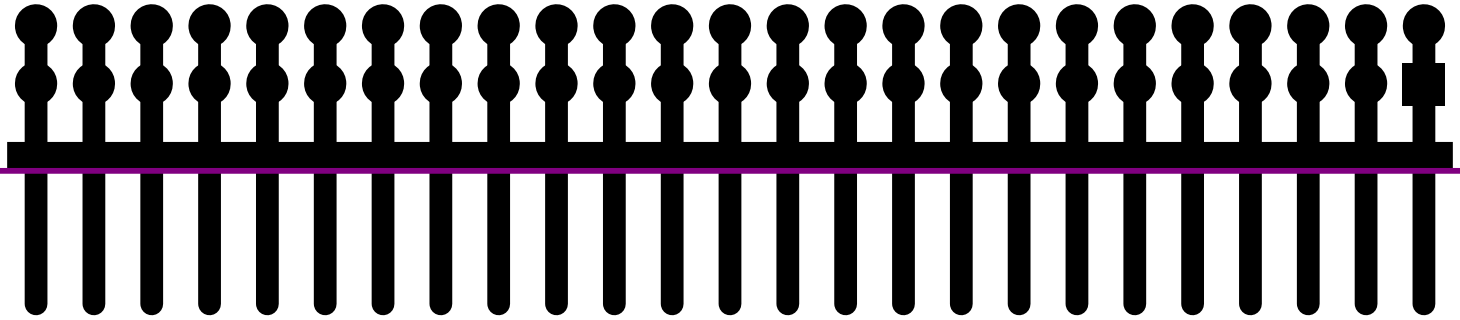
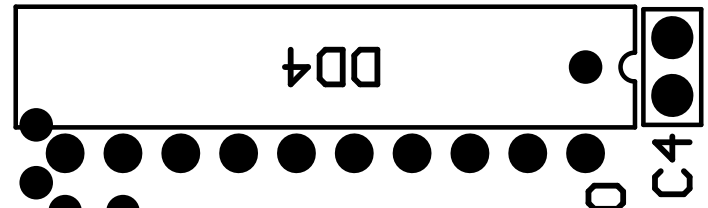
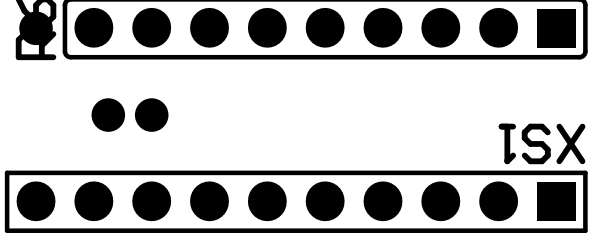
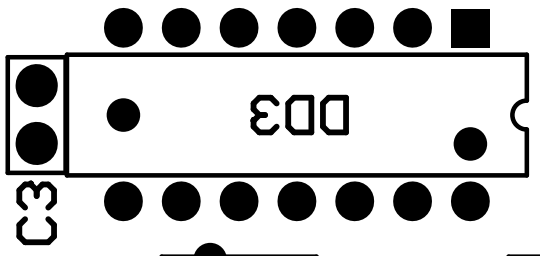
Норма/Типбо XP2

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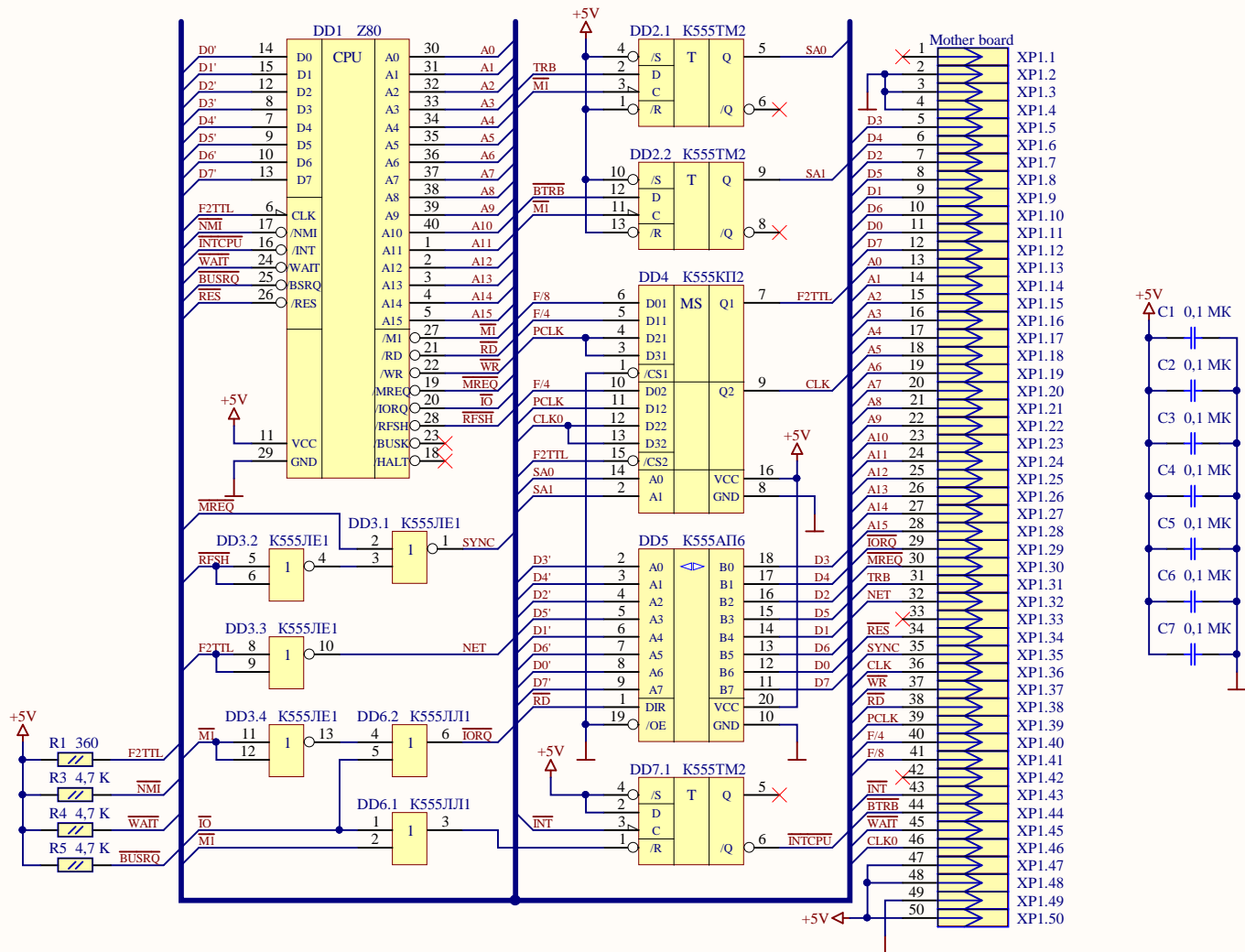
Сябр
I8085



Autor: SYR-ALEX, design: Fifan



CPUID=10



Title **Сяор. Z80 CPU. CPUID=01**

Size: A4

Number: 3

Revision: *

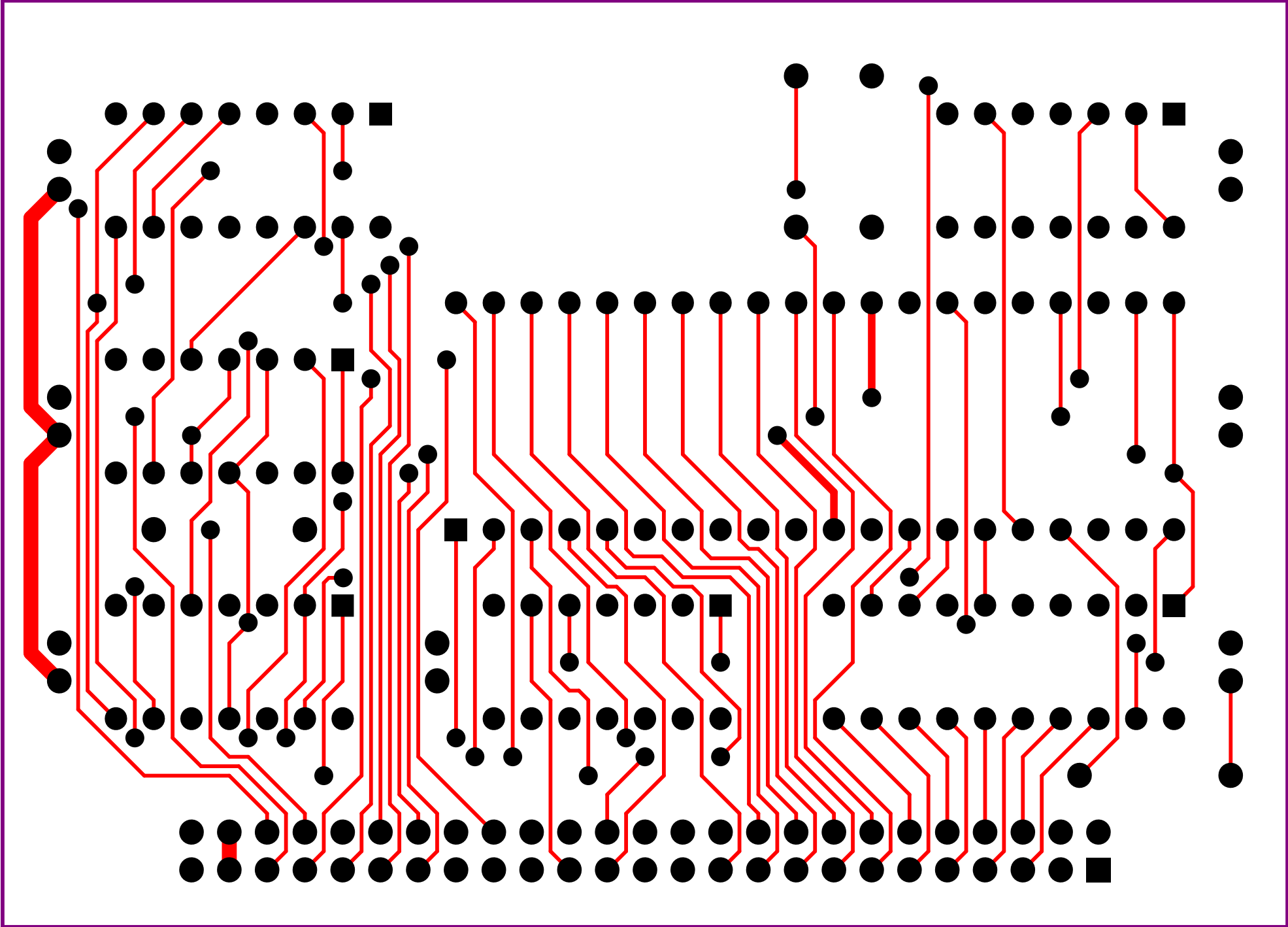
Date: 13.07.2020

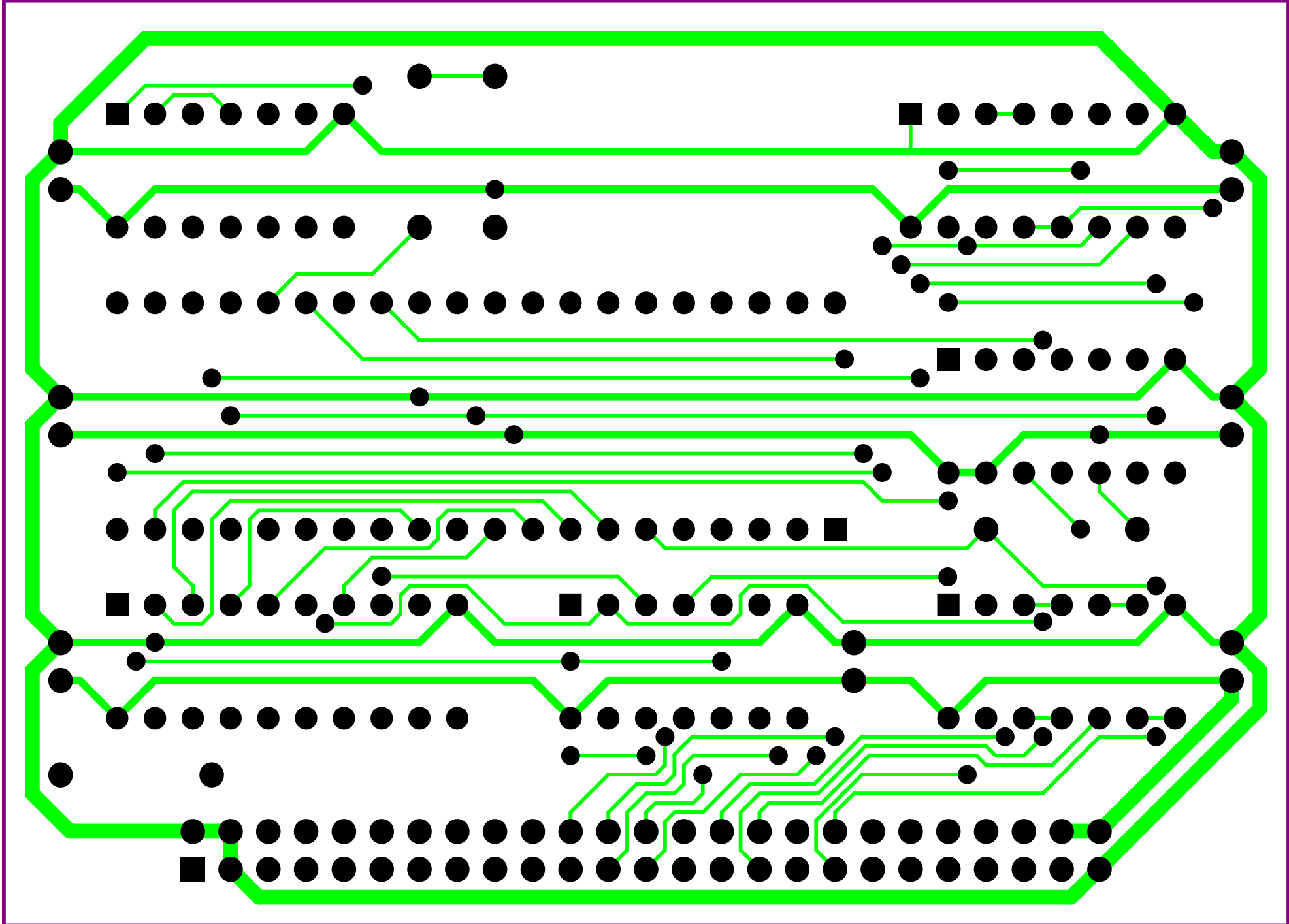
Time: 8:54:41

Sheet 3 of 4

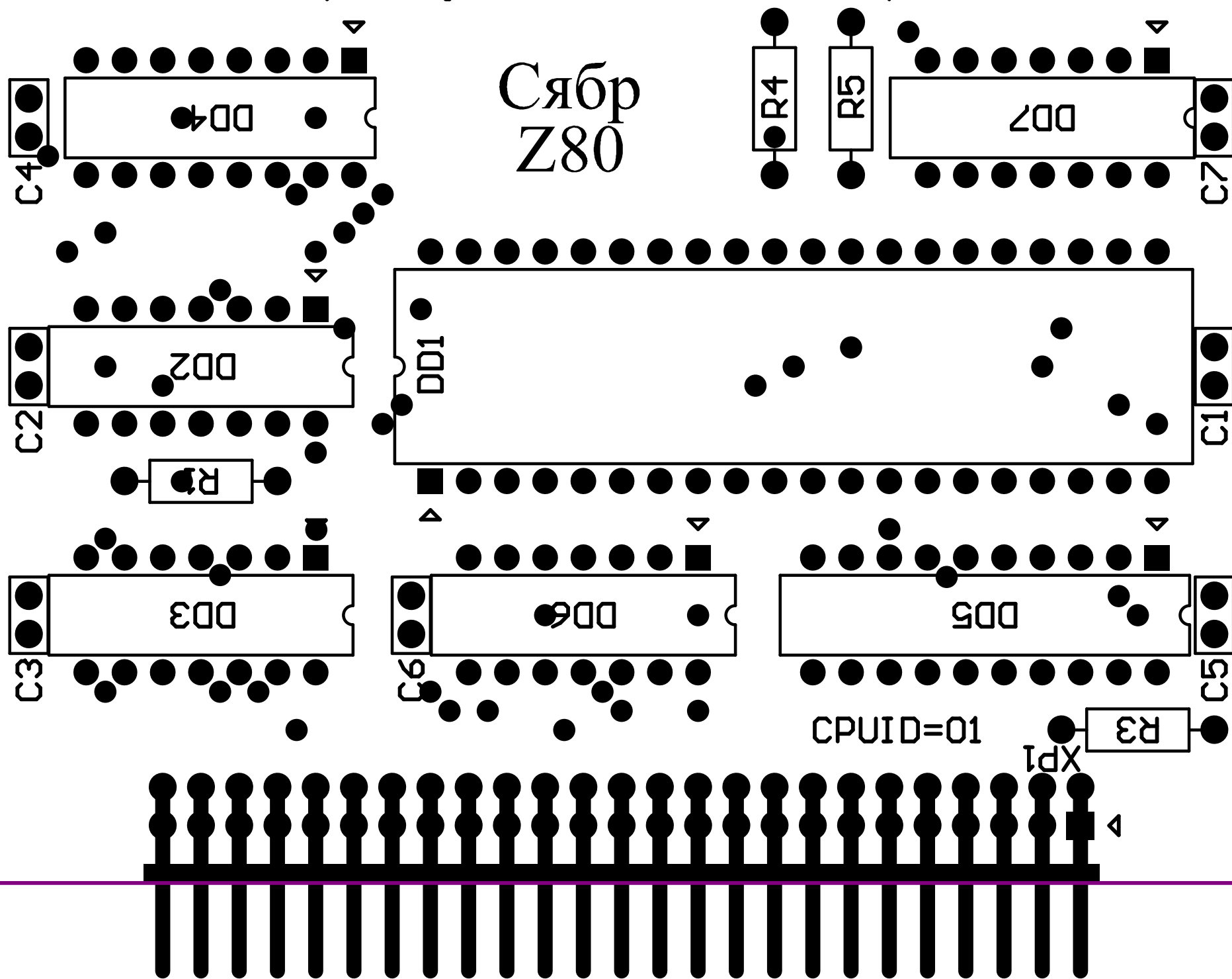
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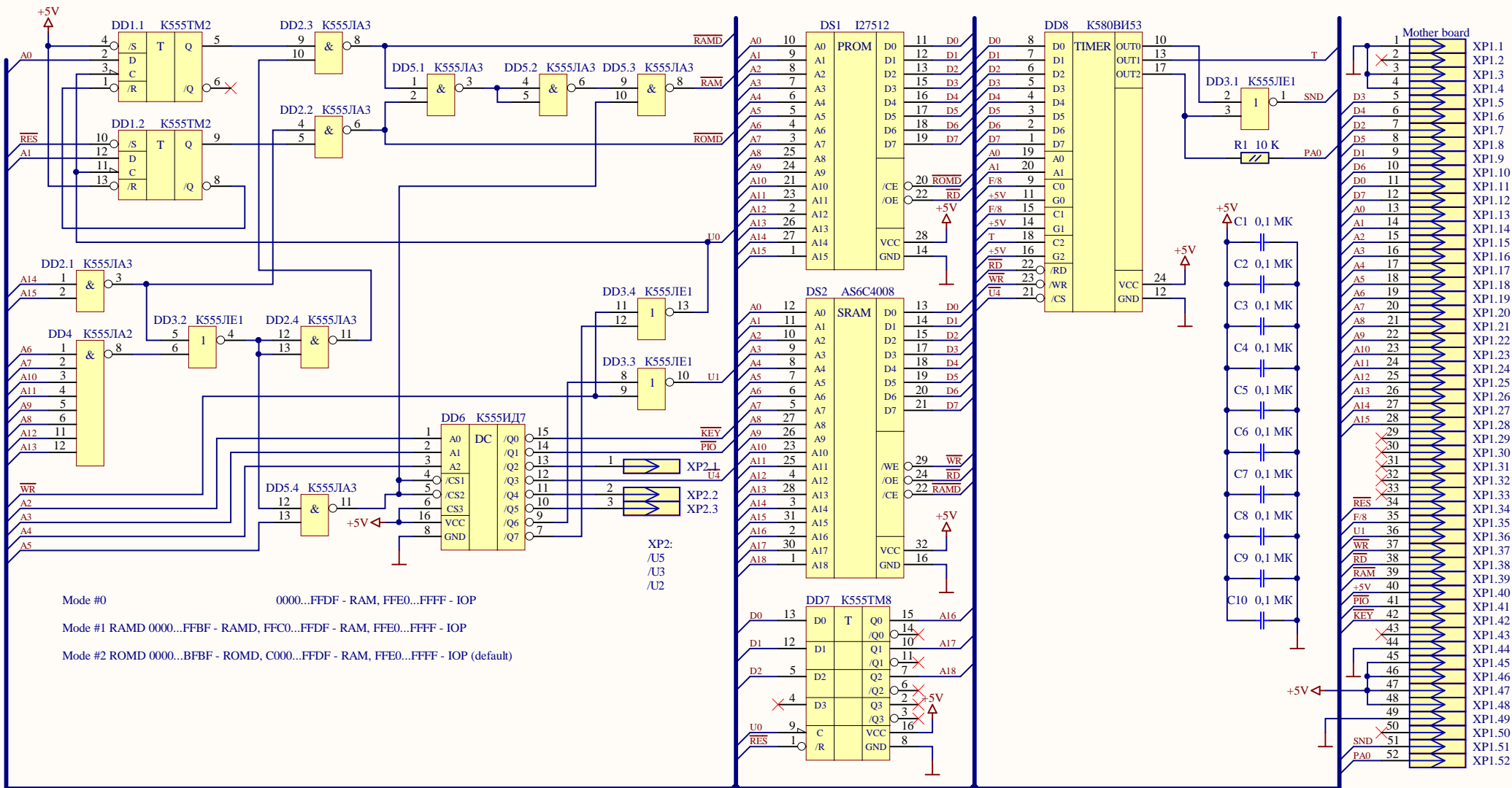






Сябр Z80





Mode #0 0000...FFDF - RAM, FFE0...FFFF - IOP

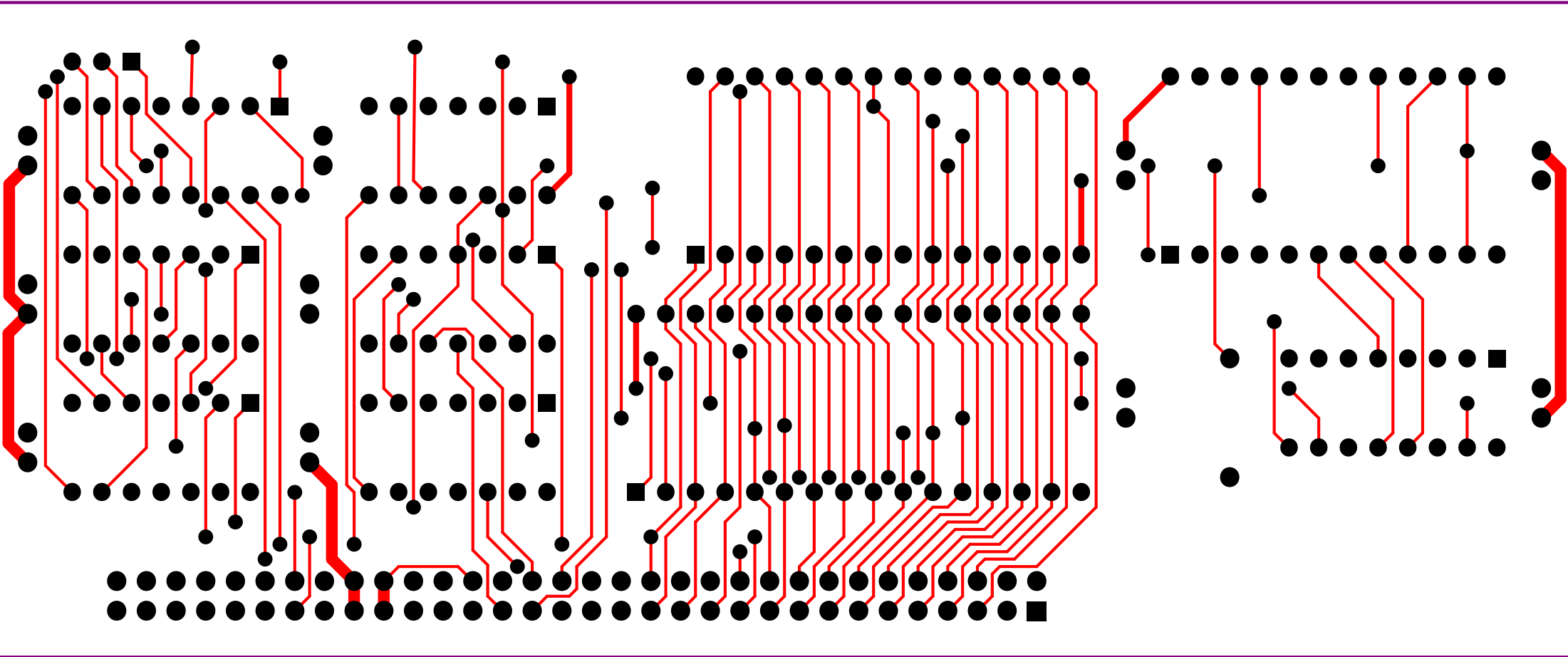
Mode #1 RAMD 0000...FFBF - RAMD, FFC0...FFDF - RAM, FFE0...FFFF - IOP

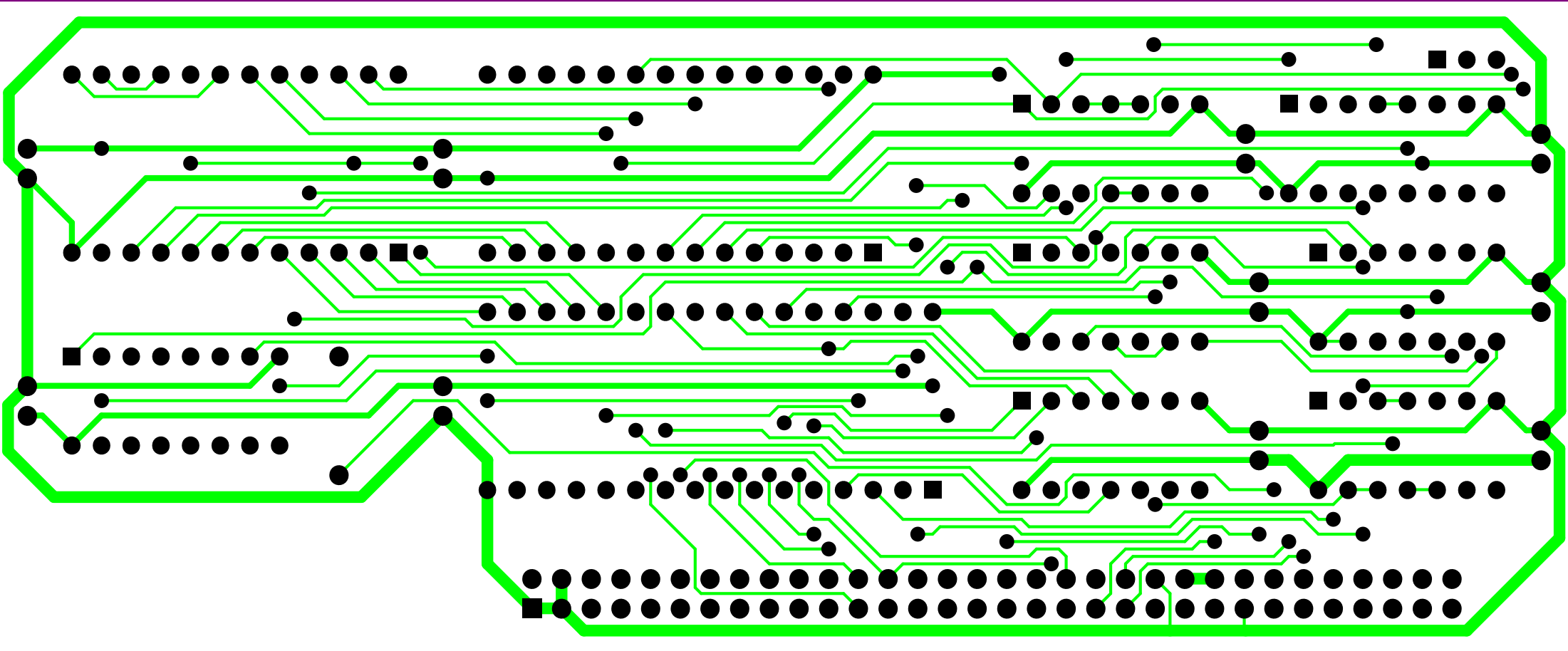
Mode #2 ROMD 0000...FFBF - ROMD, C000...FFDF - RAM, FFE0...FFFF - IOP (default)

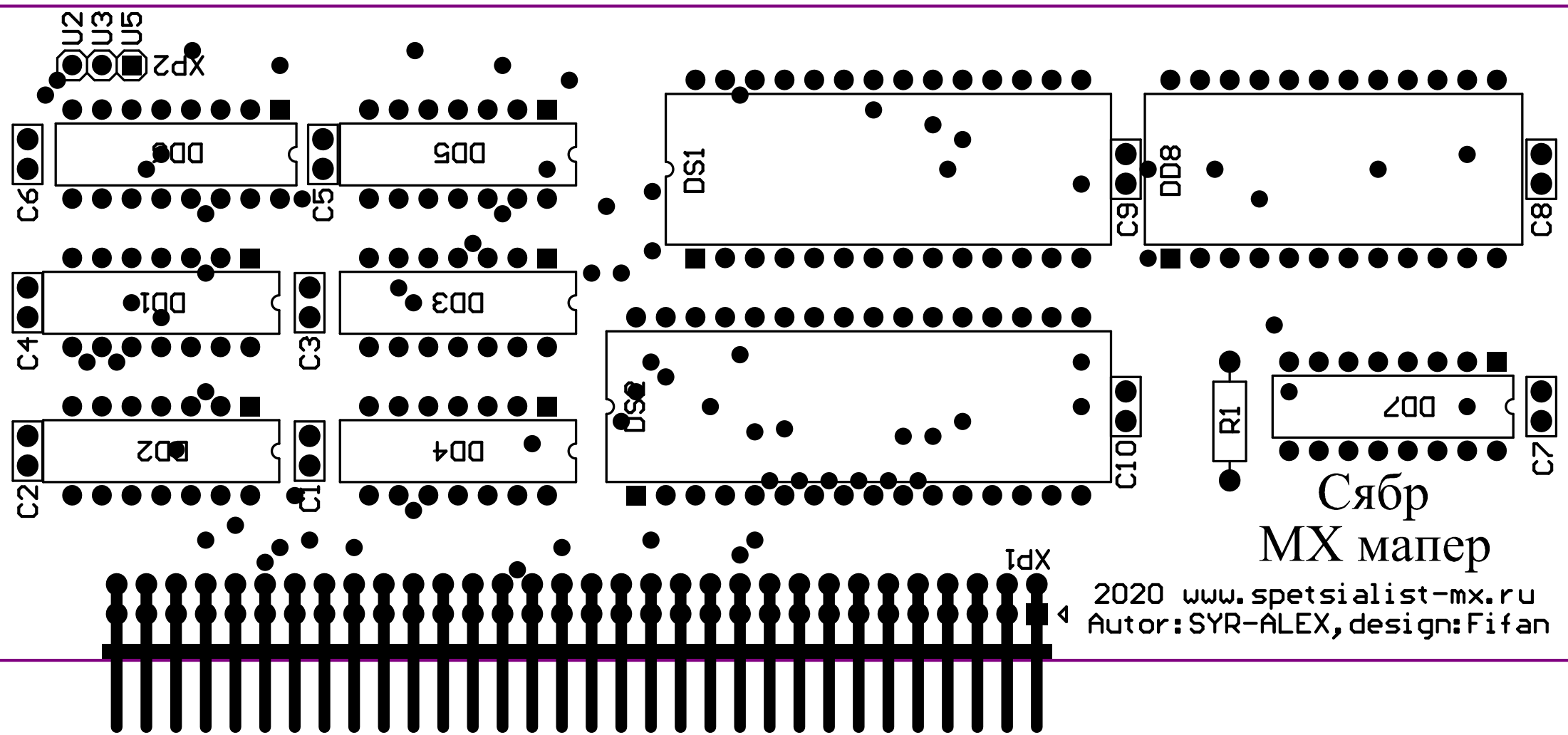
XP2:
/U5
/U3
/U2

Title Сябр. MX Mapper.		
Size: A4	Number: 4	Revision: *
Date: 13.07.2020	Time: 10:10:39	Sheet 4 of 4
File:		









Сябр
MX мапер

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A

A

B

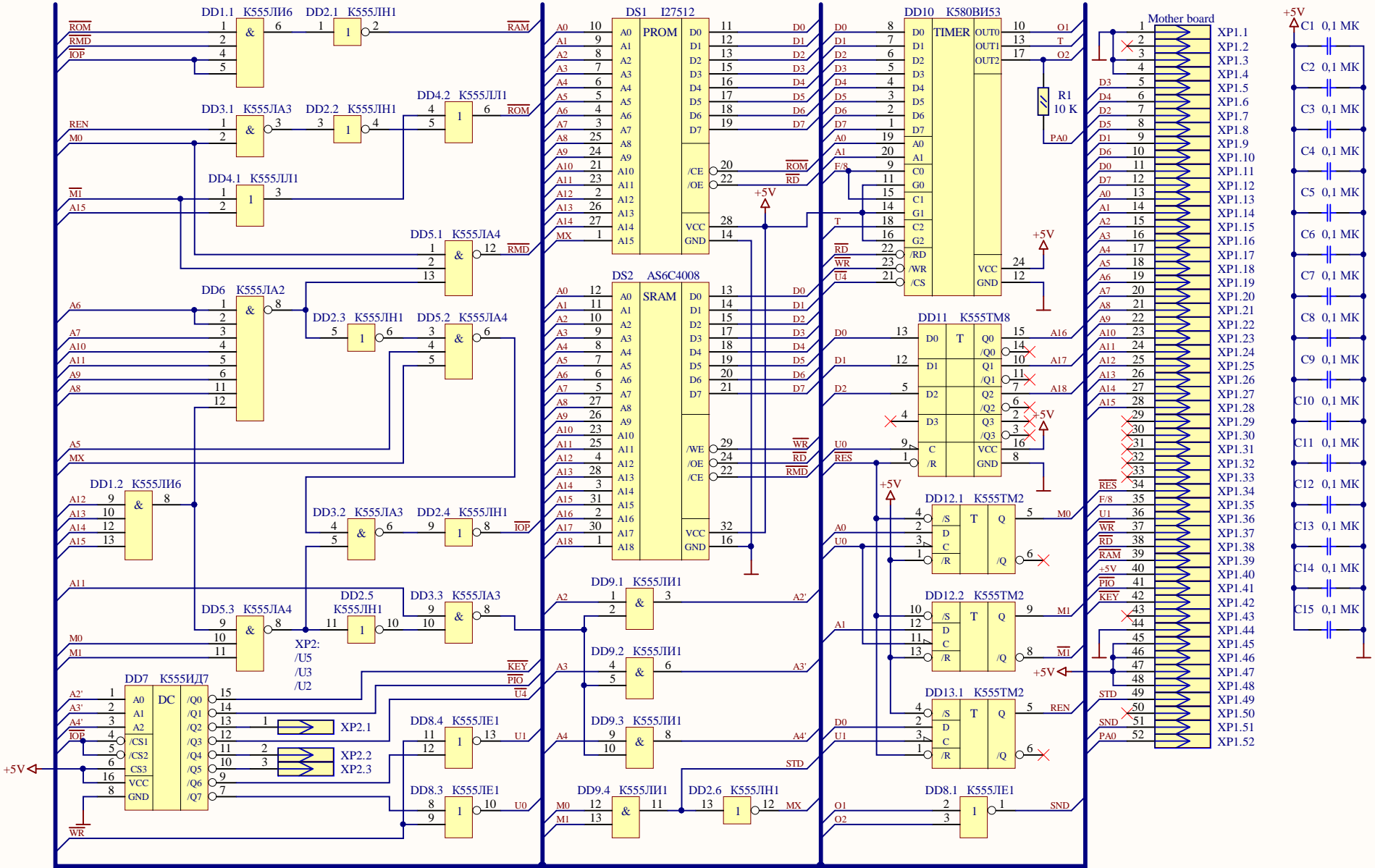
B

C

C

D

D

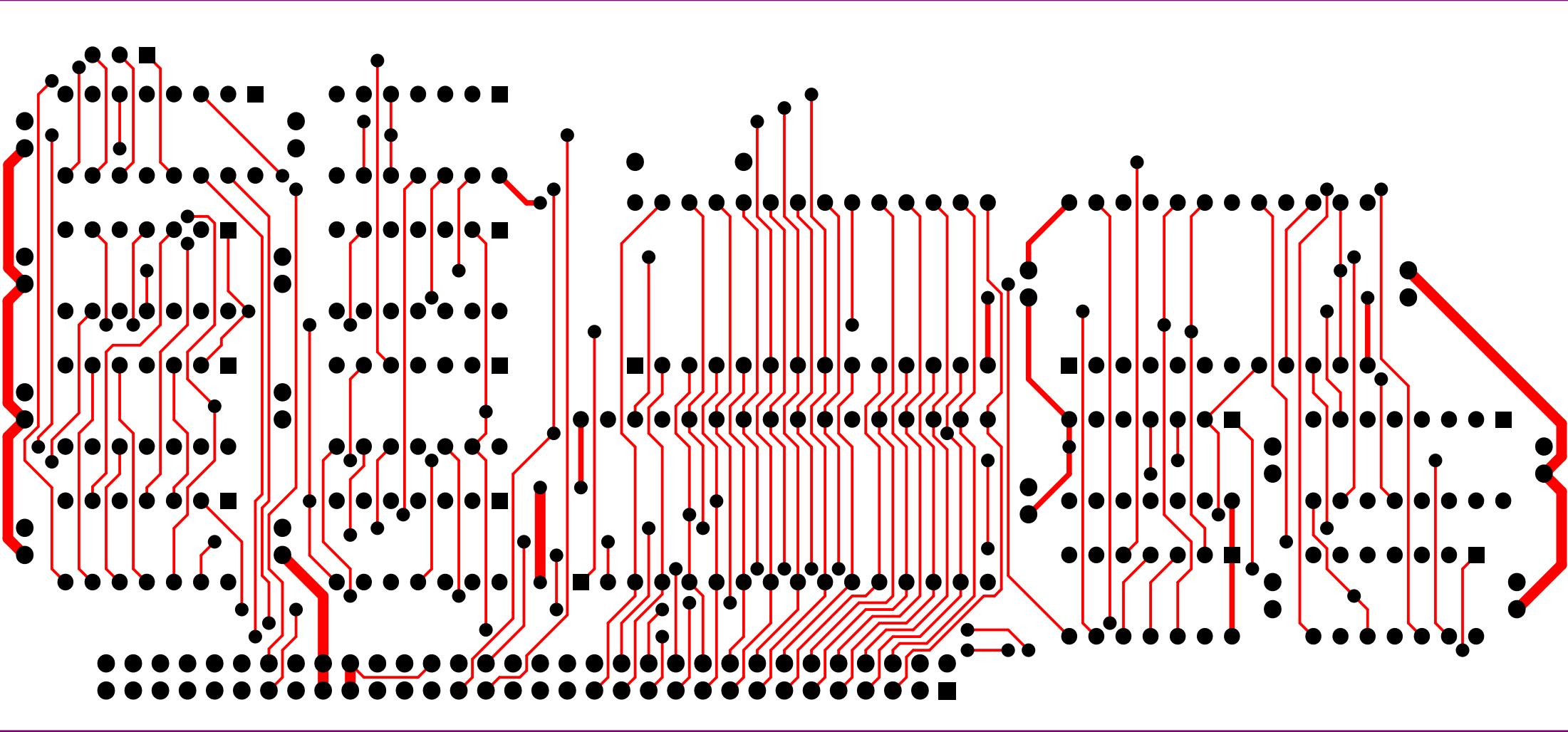


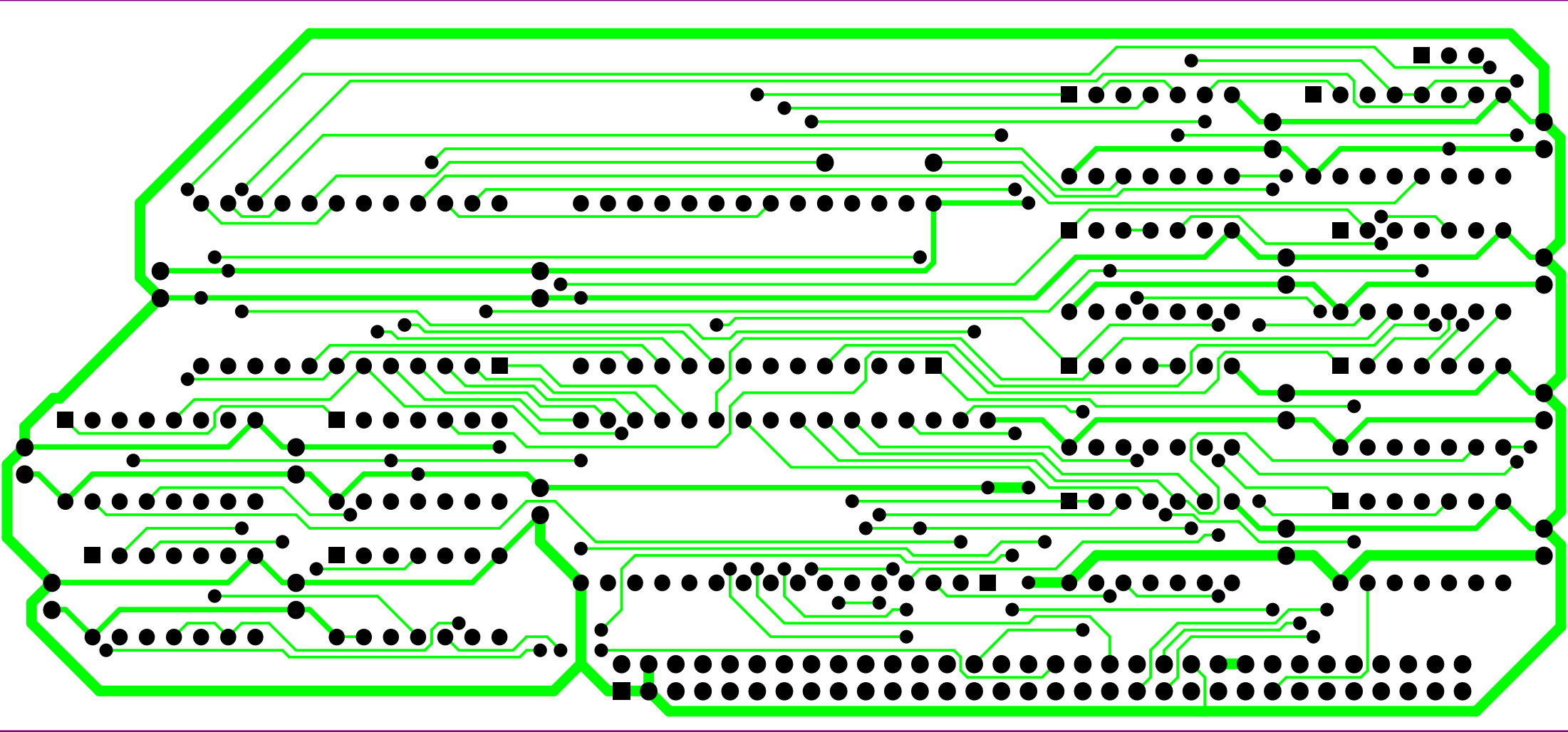
Mode #0 0000...FFDF - RAM, FFE0...FFFF - IOP
 Mode #1 RAMD 0000...FFBF - RAMD, FFC0...FFDF - RAM, FFE0...FFFF - IOP
 Mode #2 ROMD 0000...BFBF - ROMD, C000...FFDF - RAM, FFE0...FFFF - IOP
 Mode #3 ROMD 0000...7FFF - ROMD, 8000...EFFF - RAM, F000...FFFF - IOP (default)

Title **Сябр. MX2 Mapper.**

Size: A4	Number:	Revision:
Date: 13.07.2020	Time: 11:33:10	Sheet of
File:		







Сябр MX2 мапер

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